

Docket No. 243642US2TTC/phh

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hirokazu SEKINE

SERIAL NO: 10/680,179

FILED: October 8, 2003

FOR: CMOS IMAGE SENSOR

GAU:

EXAMINER:



INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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MAIER & NEUSTADT, P.C.


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STATEMENT OF RELEVANCY

Reference AO (JP 2001-268443) on Form PTO 1449:

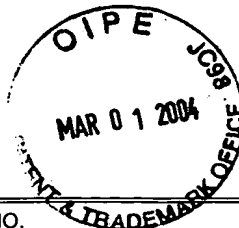
This reference shows a circuit configuration of 5 transistor pixel structure of CMOS image sensor to improve image quality.

Reference AP (JP 2001-203940) on Form PTO 1449:

This reference shows a 4 transistor pixel structure of CMOS image sensor to improve dynamic range by knee operation

Reference AR (JP 2001-298176) on Form PTO 1449:

This reference shows a fabrication process flow of 3 transistor pixel structure of CMOS image sensor to suppress leak current. Fig. 2 of this reference shows a configuration of pixel structure which has common drain region for T_1 and T_2 .



SHEET 1 OF 1

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.
243642US2TTCSERIAL NO.
10/680,179

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT
Hirokazu SEKINEFILING DATE
October 8, 2003

GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,091,449	07/18/2000	Y. MATSUNAGA, et al.			
	AB	6,570,144	05/27/2003	S. K. LEE, et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO	2001-268443	09/28/2001	JAPAN (with corr. US 6,570,144)		X
	AP	2001-203940	07/27/2001	JAPAN (with corr. EP 1 119 188)		X
	AQ	1 119 188	07/25/2001	EUROPE		
	AR	2001-298176	10/26/2001	JAPAN		X
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner

Date Considered

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.